

**REMARKS**

Claims 1 to 6 were pending in the application at the time of examination. The Examiner objected to FIG.1. The Examiner objected to the Abstract of the Disclosure. The Examiner rejected Claims 1, 3 and 6 under 35 U.S.C. 103(a) as obvious over the Adi-Tabatabai reference (US 6,093,216) in view of the Sumi et al. reference (US 5,881,288). The Examiner rejected Claim 2 under 35 U.S.C. 103(a) as obvious over the Adi-Tabatabai reference (US 6,093,216) in view of the Beadle reference (US 6,321,377). The Examiner rejected Claims 4 and 5 under 35 U.S.C. 103(a) as obvious over the Sumi et al. reference (US 5,881,288) in view of the Adi-Tabatabai reference (US 6,093,216).

**OBJECTION TO THE FIGURES**

The Examiner objected to FIG.1. as not clearly readable/viewable. Applicant has included herewith a Replacement Drawing for Figure 1, as well as a complete set of formal drawings in its Resubmission of Formal Drawings, as filed herewith to the attention of the drafts person. Applicant respectfully submits that the formal drawings are now clearly readable/viewable.

**OBJECTION TO THE ABSTRACT OF THE DISCLOSURE**

The Examiner objected to the Abstract of the Disclosure. As shown above, Applicant has amended the Abstract of the Disclosure to comply with the Examiners request. In light of the amendment to the Abstract of the Disclosure, Applicant respectfully requests the Examiner withdraw the objection to the Abstract of the Disclosure.

REJECTION OF CLAIMS 1, 3 AND 6

The Examiner rejected Claims 1, 3 and 6 under 35 U.S.C. 103(a) as obvious over the Adi-Tabatabai reference (US 6,093,216) in view of the Sumi et al. reference (US 5,881,288).

Claim 6 has been cancelled, without prejudice. Therefore Applicant respectfully submits that the rejection of Claim 6 is now moot.

Applicant's Claim 1 reads as follows, with emphasis added:

A method in a computer system for identifying at runtime a resource allocation error generated by a virtual machine compiler encompassing generating code to perform steps at runtime, said steps comprising:

obtaining a resource allocation indicator at runtime;

testing at least one bit of said resource allocation indicator, wherein said at least one bit corresponds to an allocated resource;

halting execution if said at least one corresponding bit of said resource allocation indicator is set;

setting said at least one corresponding bit, when said at least one corresponding bit is unset, to indicate allocation of said corresponding resource; and,

unsetting said at least one corresponding bit when said corresponding resource is deallocated.

As shown above, Applicant's Claim 1 specifically recites "halting execution if said at least one corresponding bit of said resource allocation indicator is set".

The Examiner notes that the Adi-Tabatabai reference fails to disclose this recited feature. However, the Examiner then uses the Sumi reference as an example of halting execution and suggests this combined with Adi-Tabatabai reference renders Applicant's Claim obvious.

Applicant respectfully submits that the mere fact that Sumi teaches halting execution in response to a specifically entered line YYY is not analogous, or in any way similar to, "halting execution if said at least one corresponding bit of said resource allocation indicator is set" as recited in Applicant's Claim 1. In other words, the proposed combination of the Adi-Tabatabai reference and the Sumi reference may, or may not yield a method of halting execution in a specific response to a specific line YYY, however it certainly does not yield a method of "halting execution if said at least one corresponding bit of said resource allocation indicator is set" as recited in Applicant's Claim 1.

In light of the discussion above, Applicant respectfully submits that the Examiner has failed to show that the Adi-Tabatabai reference, the Sumi reference or any proper combination of the Adi-Tabatabai reference and the Sumi reference, discloses, teaches or suggests "halting execution if said at least one corresponding bit of said resource allocation indicator is set" as recited in Applicant's Claim 1. Consequently, Applicant respectfully submits that Applicant's Claim 1 is patentable over the Adi-Tabatabai reference, the Sumi reference or any proper combination of the Adi-Tabatabai reference and the Sumi reference for at least this reason and Applicant respectfully requests the Examiner withdraw the rejection of Claim 1 and allow Claim 1 to issue.

Claims 2 and 3 depend, directly or indirectly, on Claim 1. Consequently, Claims 2 and 3 include all of the features and limitations of Claim 1. Therefore, Applicant respectfully submits that Claims 2 and 3 are also patentable over the Adi-Tabatabai reference, the Sumi reference or any proper combination of the Adi-Tabatabai reference and the Sumi reference.

REJECTION OF CLAIM 2

The Examiner rejected Claim 2 under 35 U.S.C. 103(a) as obvious over the Adi-Tabatabai reference (US 6,093,216) in view of the Beadle reference (US 6,321,377).

The addition of the Beadle reference (US 6,321,377) does nothing to cure the basic deficiencies of the Adi-Tabatabai reference discussed above with respect to Claim 1.

In particular, Applicant respectfully submits that the Examiner has failed to show that the Adi-Tabatabai reference, the Beadle reference or any proper combination of the Adi-Tabatabai reference and the Beadle reference, discloses, teaches or suggests "halting execution if said at least one corresponding bit of said resource allocation indicator is set" as recited in Applicant's Claim 1.

Consequently, Applicant respectfully submits that Applicant's Claim 1 is patentable over the Adi-Tabatabai reference, the Beadle reference or any proper combination of the Adi-Tabatabai reference and the Beadle reference for at least this reason.

Claim 2 depends on Claim 1. Consequently, Claim 2 includes all of the features and limitations of Claim 1. Therefore, Applicant respectfully submits that Claim 2 is also patentable over the Adi-Tabatabai reference, the Beadle reference or any proper combination of the Adi-Tabatabai reference and the Beadle reference. Therefore, Applicant respectfully requests the Examiner withdraw the rejection of Claim 2 and allow Claim 2 to issue

REJECTION OF CLAIMS 4 AND 5

The Examiner rejected Claims 4 and 5 under 35 U.S.C. 103(a) as obvious over the Sumi et al. reference (US 5,881,288) in view of the Adi-Tabatabai reference (US 6,093,216).

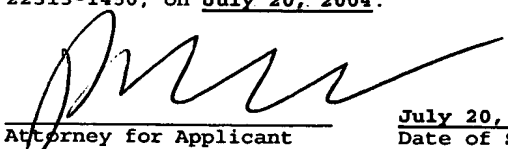
Claims 4 and 4 have been cancelled, without prejudice. Therefore Applicant respectfully submits that the rejection of Claims 4 and 5 is now moot.

CONCLUSION

For the foregoing reasons, Applicant respectfully requests allowance of all pending claims. If the Examiner has any questions relating to the above, the Examiner is respectfully requested to telephone the undersigned Attorney for Applicant.

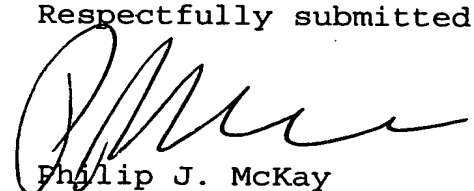
CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on July 20, 2004.

  
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Attorney for Applicant

July 20, 2004  
Date of Signature

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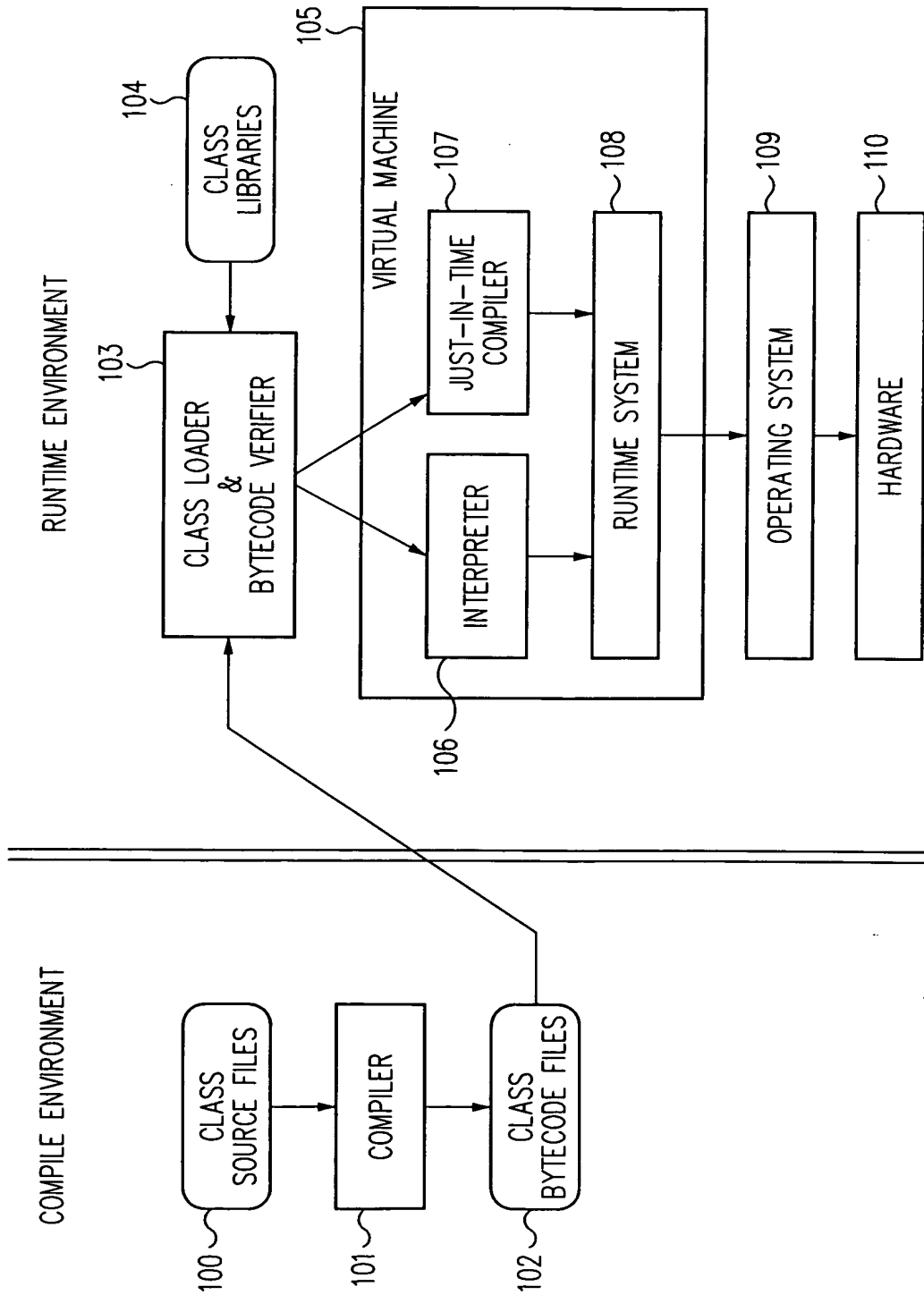


FIG. 1